EAST SEARCH   1/14/2009

L#	Hits	Search String	Databases
S1	38	(((logic adj gate$1) and (calculat$3 adj delay)) or ((logic adj gate$1) and (comput$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S2	26	(((logic adj gate and gate and delay adj time) and rise and fall) and logical adj operation) an	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S3	5	(((logic adj gate and delay adj time) and rise and fall) and logical adj operation) an	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S4	8	(((logic adj gate and gate and delay adj time) and rise and fall) and logical adj operation) an	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S5	46970	hasegawa.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S6	956	hasegawa.in. and delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S7	121	(hasegawa.in. and delay) and NEC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S8	45	(hasegawa.in. and delay) and NEC	USPAT
S9	12	((hasegawa.in. and delay) and NEC) and rise and fall	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S10	1626	delay adj calculat$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S11	26127	look adj3 table	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S12	74	(delay adj calculat$) and (look adj3 table)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S13	3	((delay adj calculat$) and (look adj3 table)) and library	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S14	473	(delay adj calculat$) and gate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S15	67	((delay adj calculat$) and gate) and fall and rise	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S16	38	((delay adj calculat$) and gate) and fall and rise) and simulat$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S17	29	((Binne and delay time) and logic cell) and rise/fall) and estimating	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S18	7	optimizing adj signal adj timing	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S19	106402	logic adj circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S20	310	(logic adj circuit$1) and (calculat$3 adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S21	37	((logic adj circuit$1) and (calculat$3 adj delay)) and (logic$2 adj delay) (information or ope	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S22	112	(logic adj circuit$1) and (comput$5 adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S23	96	(logic adj circuit$1) and (estimat$3 adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S24	468	(logic adj circuit$1) and (calculat$3 adj delay)) or ((logic adj circuit$1) and (compu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S25	56	(((logic adj circuit$1) and (calculat$3 adj delay)) or ((logic adj circuit$1) and (compu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S26	7	((logic adj circuit$1) and (calculat$3 adj delay)) and (logic$2 adj) (information or op	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S27	5	(((logic adj circuit$1) and (calculat$3 adj delay)) or ((logic adj circuit$1) and (comp	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S28	11	(((logic adj circuit$1) and (calculat$3 adj delay)) or ((logic adj circuit$1) and (comp	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S29	33722	logic adj gate$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S30	179	(logic adj gate$1) and (calculat$3 adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S31	47	(logic adj gate$1) and (comput$5 adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S32	61	(logic adj gate$1) and (estimat$3 adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S33	268	((logic adj gate$1) and (calculat$3 adj delay)) or ((logic adj gate$1) and (comput$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S34	0	(((logic adj gate$1) and (calculat$3 adj delay)) or ((logic adj gate$1) and (comput$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S35	220	(logic adj gate$1) and (delay with library)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S36	46	(logic adj circuit$1) and (delay with library) and ("connection information" or "circu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S37	0	(((logic adj circuit$1) and (delay with library)) and ("connection information" or "ciru	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S38	10	((logic adj circuit$1) and (delay with library)) and ("connection information" or "ciru	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S39	220	(logic adj circuit$1) and (delay with library)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S40	11	((logic adj circuit$1) and (delay with library)) and "logic information"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB